

Cofc



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:  
Paul Garnett

Patent No.: 6,862,645  
Issued: March 1, 2005

Serial No.: 09/766,246  
Filed: January 19, 2001

For: COMPUTER SYSTEM



Atty. Docket No.: 5181-80100

Group Art Unit: 2111

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

B. Noel Kivlin  
Signature of Representative

3-22-06  
Date

Signature

TRANSMITTAL LETTER

Certificate  
MAR 28 2006  
of Correction

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

Transmitted herewith for filing in the captioned case are the following:

- (1) Form PTO-1050, **submitted in duplicate**. Errors which occur at important points in the captioned patent or which may otherwise affect the understanding or interpretation of the patent are thereon corrected.
- (2) A return postcard to acknowledge receipt of these materials. Please stamp and return this postcard to the undersigned.

MAR 28 2006

All of the errors shown in PTO-1050 are due to Patent Office oversights. A Certificate of Correction is requested under 35 U.S.C. §254. Applicants believe that no fees are required; however, should any fees be required, please deduct them from Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C. Deposit Account No. 501505\5181-80100/BNK.

Respectfully submitted,



B. Noël Kivlin  
Reg. No. 33,929  
Attorney for Applicant(s)

Meyertons, Hood, Kivlin,  
Kowert & Goetzel, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398  
(512) 853-8840

Date: 3-22-06

MAR 28 2006

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,862,645

DATED: March 1, 2005

INVENTOR: Paul Garnett

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9,

Lines 42-44, please delete "register read/writeable and is configured to store data representative of a processor identification," and substitute

-- register comprising:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, --;

Line 47, please delete "sets into its processor" and substitute -- sets into said first field of its processor --;

Column 10,

Line 10, please delete "that includes a plurality" and substitute -- that comprises a plurality --;

Lines 14-16, please delete "is read/writeable and is configured to store data representative of a processor identification," and substitute

-- register comprises:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, --;

Line 18, please delete "into said processor" and substitute -- into said first field of said processor --;

Lines 21 - 22, please delete "of a processor identification." and substitute -- of a processor version. --;

-----  
MAILING ADDRESS OF SENDER:

B. Noel Kivlin, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,862,645

No. of add'l. copies: 1

MAR 28 2006

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,862,645

DATED: March 1, 2005

INVENTOR: Paul Garnett

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10,

Lines 29 – 30, please delete “each of which processing” and substitute -- each of said processing --;

Line 31, please delete “processor, at least” and substitute -- processor, wherein at least --;

Line 33, please delete “register said method comprising the steps of” and substitute

-- register comprising:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, said method comprising: --;

Lines 38 - 39, please delete “into said processor” and substitute -- into said first field of processor --;

Lines 41 - 42, please delete “said processor identification.” and substitute --said processor version. --;

Line 53, please delete “a processing sets” and substitute -- a processing set --.

Lines 62 - 63, please delete “method comprising the steps of:” and substitute -- method comprising: --;

## MAILING ADDRESS OF SENDER:

B. Noël Kivlin, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,862,645

No. of add'l. copies: 1

MAR 28 2006

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,862,645

DATED: March 1, 2005

INVENTOR: Paul Garnett

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11,

Lines 2 - 4, please delete "register which is read/writeable and has data representative of a processor" and substitute

-- register comprising:

a first field which is read/writeable and which has data representative of a processor version of the processor stored therein; and

a second field which has data representative of a configuration of the processor stored therein, --;

Line 7, please delete "via said I/O bus into said processor" and substitute -- via said I/O bus into said first field of said processor --;

Column 12,

Lines 2 - 3, please delete "said processor identification." and substitute -- said processor version. --.

## MAILING ADDRESS OF SENDER:

B. Noel Kivlin, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,862,645

No. of add'l. copies: 1

MAR 28 2006

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,862,645

DATED: March 1, 2005

INVENTOR: Paul Garnett

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 9,

Lines 42-44, please delete "register read/writeable and is configured to store data representative of a processor identification," and substitute

-- register comprising:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, --;

Line 47, please delete "sets into its processor" and substitute -- sets into said first field of its processor --;

Column 10,

Line 10, please delete "that includes a plurality" and substitute -- that comprises a plurality --;

Lines 14-16, please delete "is read/writeable and is configured to store data representative of a processor identification," and substitute

-- register comprises:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, --;

Line 18, please delete "into said processor" and substitute -- into said first field of said processor --;

Lines 21 - 22, please delete "of a processor identification." and substitute -- of a processor version. --;

## MAILING ADDRESS OF SENDER:

B. Noel Kivlin, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,862,645

No. of add'l. copies: 1

MAR 28 2006

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,862,645

DATED: March 1, 2005

INVENTOR: Paul Garnett

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 10,

Lines 29 – 30, please delete “each of which processing” and substitute -- each of said processing --;

Line 31, please delete “processor, at least” and substitute -- processor, wherein at least --;

Line 33, please delete “register said method comprising the steps of” and substitute

-- register comprising:

a first field which is read/writeable and which is configured to store data representative of a processor version of the processor; and

a second field which is configured to store data representative of a configuration of the processor, said method comprising: --;

Lines 38 - 39, please delete “into said processor” and substitute -- into said first field of processor --;

Lines 41 - 42, please delete “said processor identification.” and substitute --said processor version. --;

Line 53, please delete “a processing sets“ and substitute -- a processing set --.

Lines 62 - 63, please delete “method comprising the steps of:” and substitute -- method comprising: --;

-----  
MAILING ADDRESS OF SENDER:

B. Noel Kivlin, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,862,645

No. of add'l. copies: 1

MAR 28 2005

## UNITED STATES PATENT AND TRADEMARK OFFICE

**CERTIFICATE OF CORRECTION**

PATENT NO.: 6,862,645

DATED: March 1, 2005

INVENTOR: Paul Garnett

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 11,

Lines 2 - 4, please delete "register which is read/writeable and has data representative of a processor" and substitute

-- register comprising:

a first field which is read/writeable and which has data representative of a processor version of the processor stored therein; and

a second field which has data representative of a configuration of the processor stored therein, --;

Line 7, please delete "via said I/O bus into said processor" and substitute -- via said I/O bus into said first field of said processor --;

Column 12,

Lines 2 - 3, please delete "said processor identification." and substitute -- said processor version. --.

## MAILING ADDRESS OF SENDER:

B. Noel Kivlin, Esq.,  
MEYERTONS, HOOD, KIVLIN, KOWERT & GOETZEL, P.C.  
P.O. Box 398  
Austin, Texas 78767-0398

PATENT NO. 6,862,645

No. of add'l. copies: 1

**Certificate of Correction (PTO Form 1050)**

MAR 28 2005